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December 21, 1999

ATTORNEY DOCKET NO.: 04329.2210

**Box Patent Application  
Assistant Commissioner for Patents  
Washington, D.C. 20231**

New U.S. Patent Application

Title: SEMICONDUCTOR DEVICE AND  
METHOD OF MANUFACTURING THE SAME

Inventors: 1) Katsuhiko HIEDA and 2) Kazuhiro EGUCHI

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. A check for \$932 representing a \$892 filing fee and \$40 for recording the Assignment.
2. Application - 51 pages, including 4 independent claims and 23 claims total.
3. Drawings - 15 sheets of formal drawings, containing 53 figures.
4. Declaration and Power of Attorney.
5. Recordation Form Cover Sheet and Assignment to Kabushiki Kaisha Toshiba.

Applicants claim the right to priority based on Japanese Patent Application Nos. 10-365491, filed on December 22, 1998 and 11-336605, filed on November 26, 1999.

Please accord this application a serial number and filing date and record and return the Assignment to the undersigned.

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

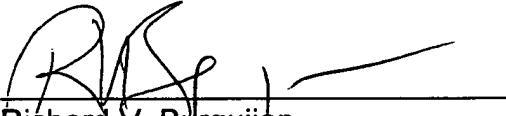
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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By:

  
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RVB/FPD/sci  
Enclosures

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